

FIG. 1

Serial Mapping System

FIG. 2

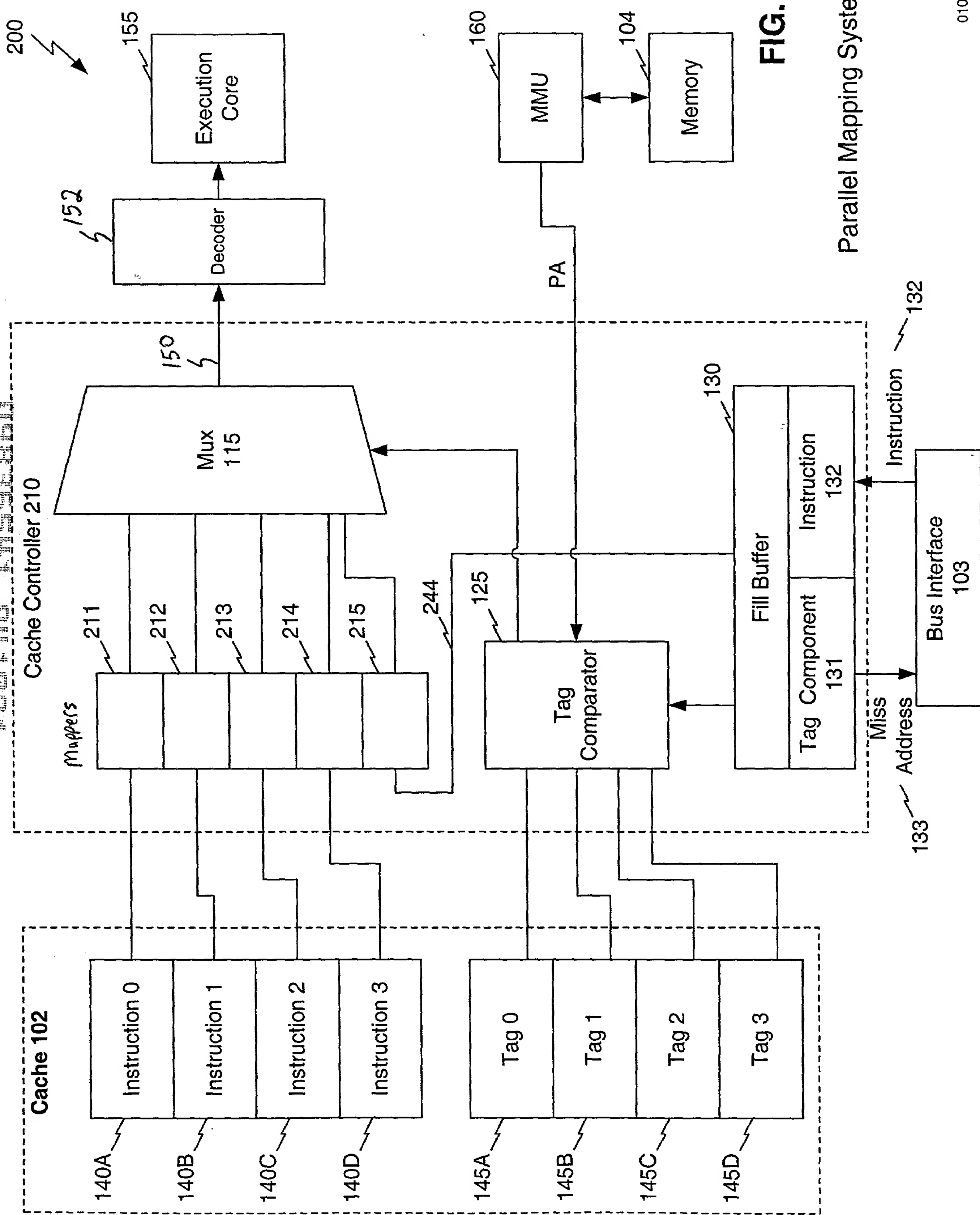


FIG. 2

Parallel Mapping System

$t \rightarrow$

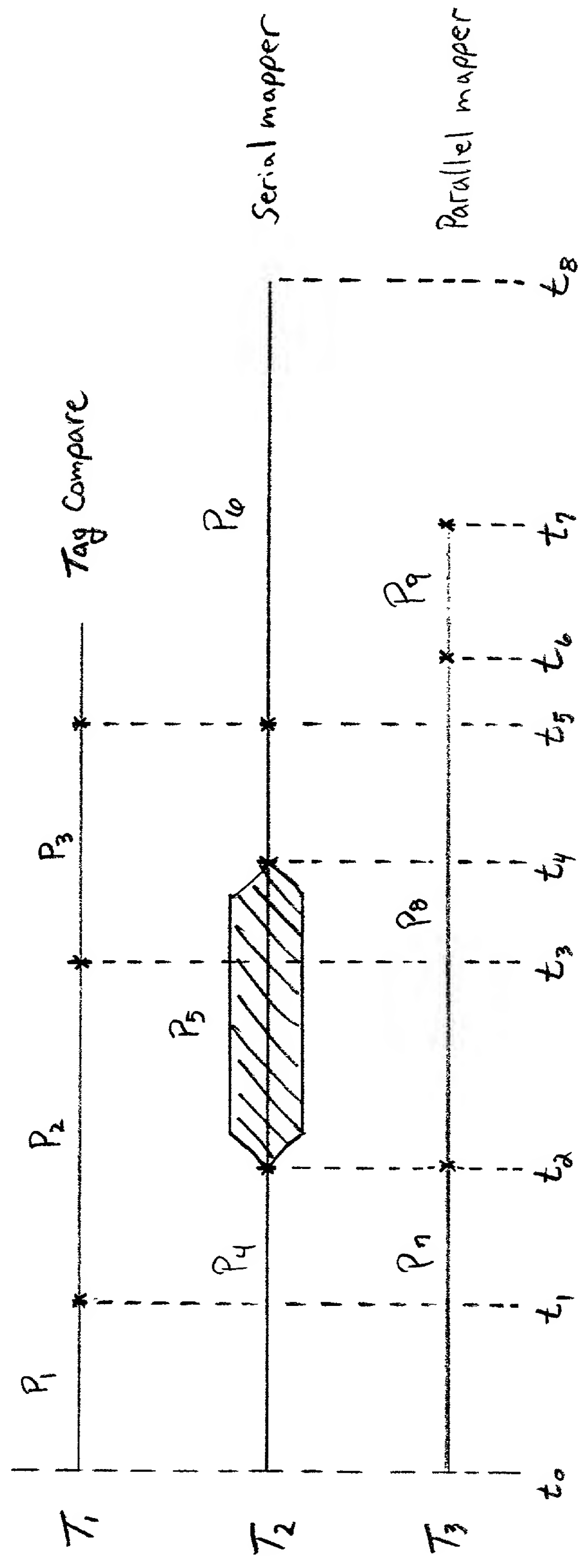


FIG. 3

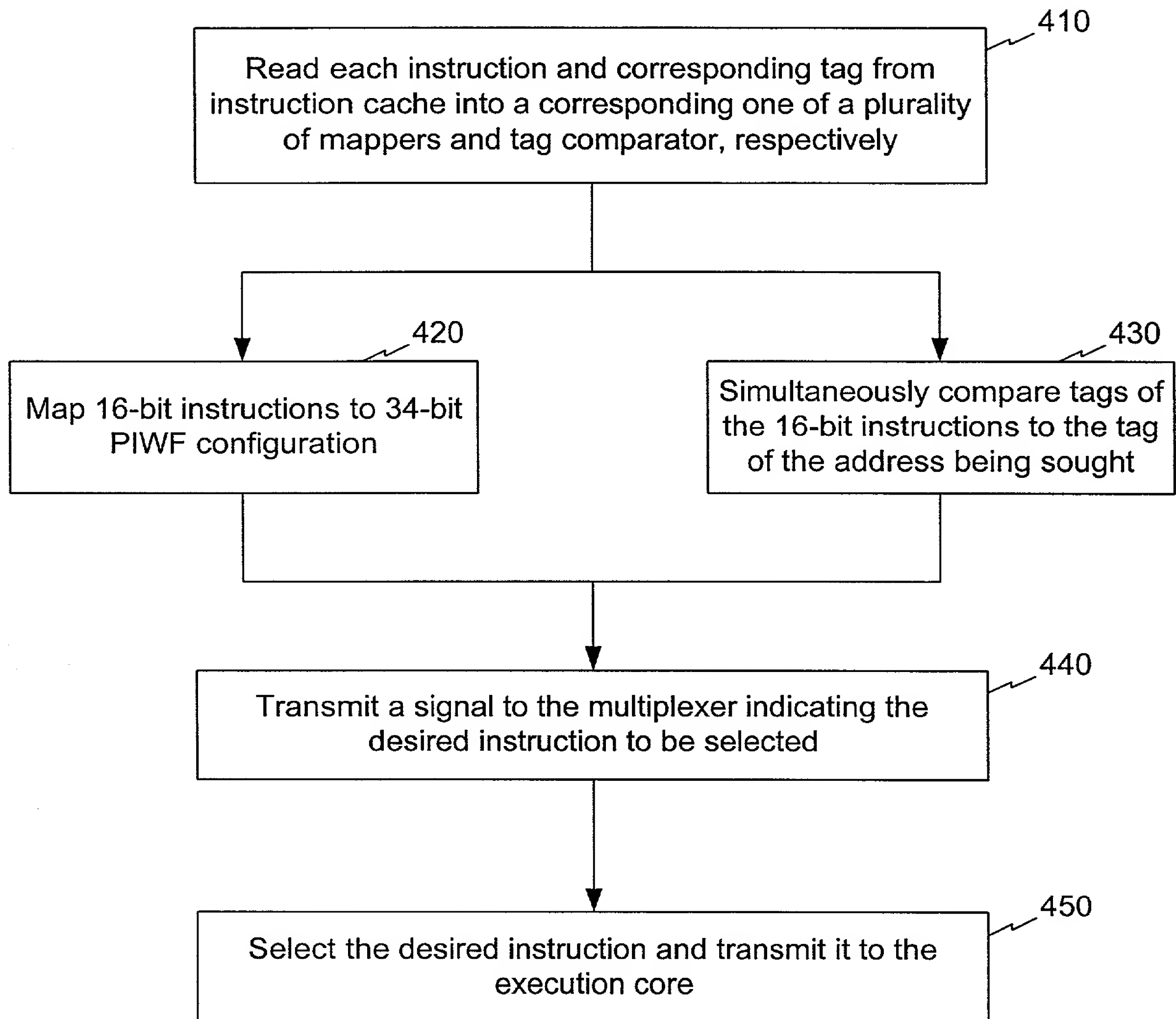


FIG. 4

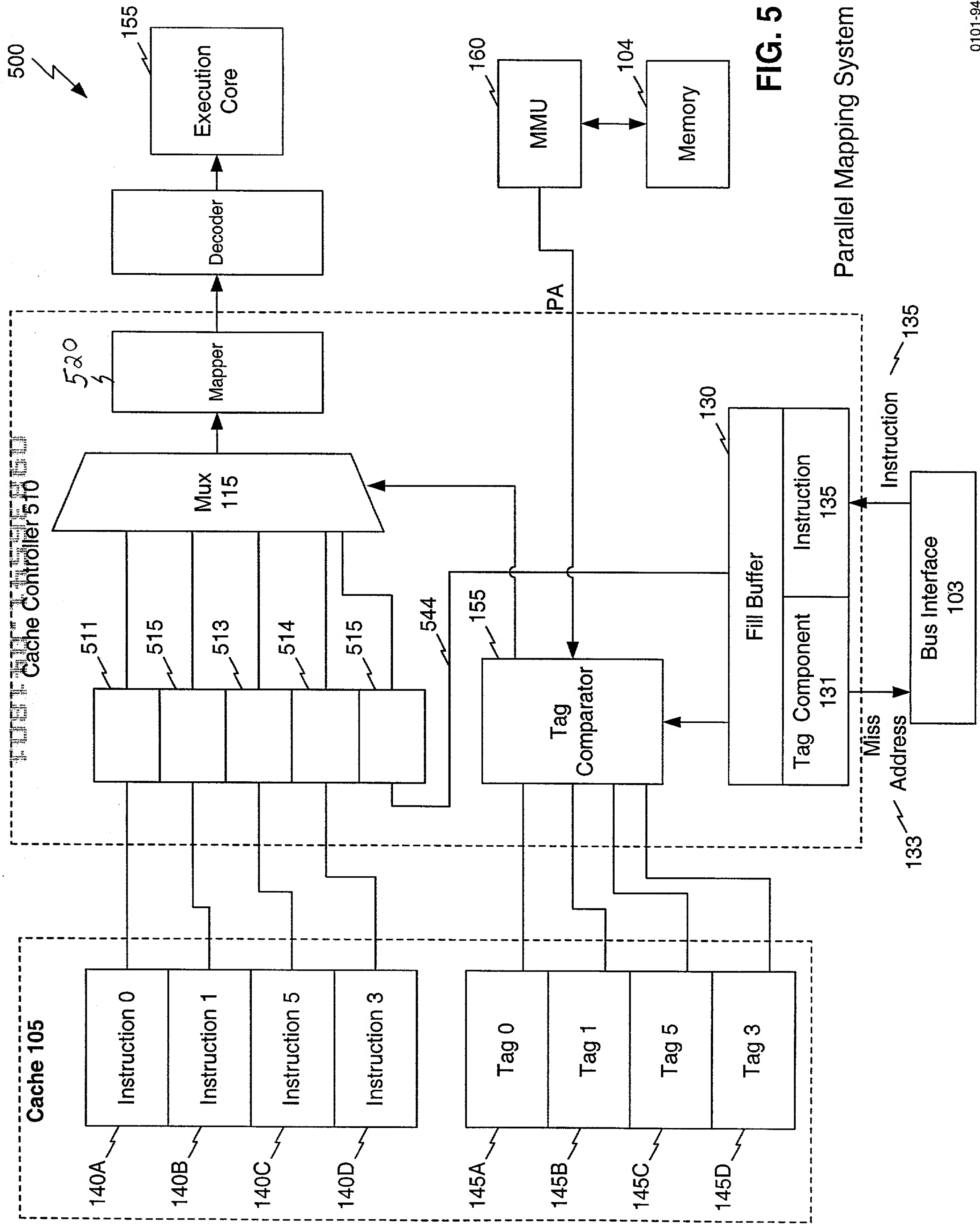


FIG. 5

Parallel Mapping System